

## EAST Search History

| Ref # | Hits | Search Query  | DBs  | Default Operator | Plurals | Time Stamp       |
|-------|------|---|--|------------------|---------|------------------|
| L1    | 0    | circuit adj3 board and interconnect and laminating and etching and metal adj3 layer and drilling and milling and castellation and via | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2007/02/20 09:44 |
| L2    | 2    | circuit adj3 board and interconnect and laminating and etching and metal adj3 layer and drilling and castellation                     | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2007/02/20 09:44 |
| L3    | 6    | circuit adj3 board and interconnect and etching and metal adj3 layer and drilling and castellation                                    | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2007/02/20 09:45 |
| L4    | 6    | circuit adj3 board and interconnect and etching and metal and layer and drilling and castellation                                     | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2007/02/20 09:45 |
| L5    | 42   | circuit adj3 board and interconnect and etching and metal and layer and castellation  | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2007/02/20 09:45 |